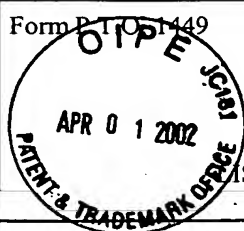


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Examiner's Initials		Document Number	Date	Name	Class	Subclass	Filing Date If appropriate
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	AQ							

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>SAR</i>	AR	Analysis and Optimization of Accumulation-Mode Varactor for RF ICs - Soorapanth et al 1998 Symposium on VLSI Circuits Digest of Technical Papers, pp. 32-33, June 1998
<i>SAR</i>	AS	UTSi CMOS: A Complete RF SOI Solution - Analog Integrated Circuits and Signal Processing, 25, 171-179, 2000 Kluwer Academic Publishers
<i>SAR</i>	AT	Design of High-Q Varactors for Low-Power Wireless Applications Using a Standard CMOS Process - Porret et al - IEEE Journal of Solid-State Circuits, Vol. 35, No. 3, March 2000
<i>SAR</i>	AU	A Metal-Oxide-Semiconductor Varactor - Svelto et al - IEEE Electron Device Letters, Vol. 20, No. 4 April 1999
	AV	

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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.